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/*          PAL U15 FOR PROOF MODULE CONTROL VERSION 1*/
#define JEDFILE "DEMO_DQ2.JED"
#define DESIGNER James Jolly
#define COMPANY SOEST Design and Engineering Group
#define PARTNUM PEEL22V10 (P22V10, board refdes: Uxx)
#define REVISION 1.00 (Last change: 18 Sept 2005)
#define COMMENTS \
\
Includes TEST VECTORS.

/*
*
*/

palname (in CLOCK,          /* com reg clock */
        DQ, DQTEMP,        /* inputs */
        LDQ1, LDQ2_, LTEMP1, LTEMP2_,
        INITCNT_, INITTEMP_,
        ENDPER, RES_,
        PORST_;
        reg PERCNT_;
        io STRTEMP_, STRCNT_,      /* I/O outputs */
        PERIODS,
        SELECT, CLKOUT, O18, SELDQ_, SELDQTEMP_,
        RESET_)

{
    PERCNT_.ck = CLOCK;
    PERCNT_.pre = 0;
    PERCNT_.aclr = !RES_;

    STRTEMP_.oe = STRCNT_.oe = PERCNT_.oe = PERIODS.oe = RESET_.oe = 1;
    SELECT.oe = CLKOUT.oe = O18.oe = 1;
    SELDQ_.oe = SELDQTEMP_.oe = 0;

/*      EQUATIONS:      */

    STRCNT_ = INITCNT_ & !(LDQ1 & LDQ2_);

    STRTEMP_ = INITTEMP_ & !(LTEMP1 & LTEMP2_);

    PERIODS = !PERCNT_ & CLOCK;

    PERCNT_ = ENDPER;          /* latched */

    RESET_ = RES_ & PORST_;

    !SELECT = SELDQTEMP_ & !(SELDQ_ & SELECT);

    CLKOUT = (DQ & SELECT) | (DQTEMP & !SELECT); /* feeds back to pin 1
*/

    putpart("P22V10", JEDFILE ,
            CLOCK, DQ, DQTEMP, ENDPER, LDQ1, LDQ2_, LTEMP1, LTEMP2_,
            INITCNT_, INITTEMP_, RES_, GND,

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    PORST_, STRTEMP_, STRCNT_, PERCNT_, SELECT, O18, PERIODS, SELDQ_,  
SELDQTEMP_, CLKOUT, RESET_, VCC);  
}
```