

```

/*          PAL U15 FOR PROOF MODULE CONTROL VERSION 1*/
#define JEDFILE  "DEMO_CN1.JED"
#define DESIGNER James Jolly
#define COMPANY SOEST Design and Engineering Group
#define PARTNUM  PEEL22V10   (P22V10, board refdes: Uxx)
#define REVISION 1.00   (Last change:  18 Sept 2005)
#define COMMENTS \
\
        Includes TEST VECTORS.

/*
*
*/

palname (in DUMMY0,          /* com reg clock */
        WDENA,  WDRES_, STROBE,  /* inputs */
        DUMMY1, DUMMY2, DUMMY3,
        DUMMY4, DUMMY5, DUMMY6,
        DUMMY7, SD_;
        io  EN_, DUMIO1, DUMIO2,  /* I/O outputs */
        DUMIO3, DUMIO4, DUMIO5,
        DUMIO6, DUMIO7,
        WDCLR, RESET_)

{
    EN_.oe      = DUMIO1.oe = DUMIO2.oe = DUMIO3.oe = DUMIO4.oe = 1;
    DUMIO5.oe   = DUMIO6.oe = DUMIO7.oe = WDCLR.oe  = RESET_.oe = 1;

/*      EQUATIONS:          */

    !EN_ = SD_;

    WDCLR = STROBE;

    !RESET_ = (!WDRES_ & WDENA);

    putpart("P22V10", JEDFILE ,
        DUMMY0, STROBE, WDENA,  WDRES_, DUMMY1, DUMMY2, DUMMY3, DUMMY4, DUMMY5,
        DUMMY6, DUMMY7, GND,
        SD_, EN_, DUMIO1, DUMIO2, DUMIO3, DUMIO4, DUMIO5, DUMIO6, DUMIO7, WDCLR,
        RESET_, VCC);
}

```